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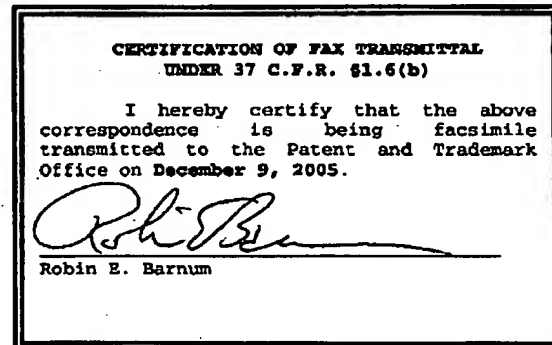
RESPONSE UNDER 37 CFR 1.116  
EXPEDITED PROCEDURE  
EXAMINING GROUP 2128

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Swoboda et al                      Art Unit: 2128  
Serial No.: 09/943,599                      Examiner: Akash Saxena  
Filed: August 30, 2001                      Docket: TI-30481  
For: CORRELATING ON-CHIP DATA PROCESSOR TRACE INFORMATION FOR  
EXPORT

AMENDMENT UNDER 37 C.F.R. 1.116

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450



Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated September 9, 2005. These amendments are respectfully submitted as a full and complete response to that Action.

Please amend the above referenced application as follows: